ABSTRACT OF THE DISCLOSURE

Power saving means is provided for personal digital assistants by lowering an operating clock for a display and accessing a memory with a low clock via a bus.

5   A CPU 1 and a display controller 8 share a RAM 3 via a bus in a mobile telephone device. A continuation of an uncontrolled state during a predetermined period initiates an interruption processing which changes an operating clock for the CPU 1 from a normal clock to a peripheral clock of a low

10   speed for a display 10, resulting in power saving in the CPU 1 during the time that any processing is not performed, while ordinarily refreshing the images on the display 10 enables normal images to be displayed on the display 10.